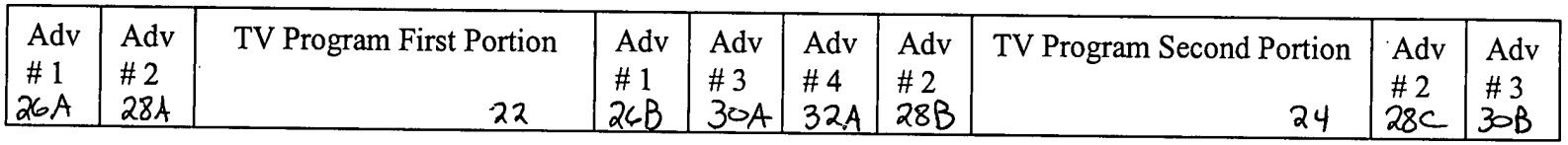

Figure 1A: Prior Art

Figure 1B

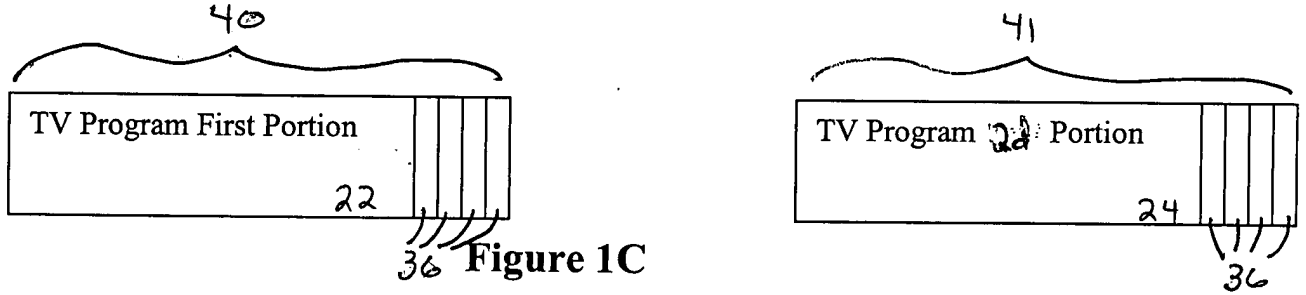

Figure 1C

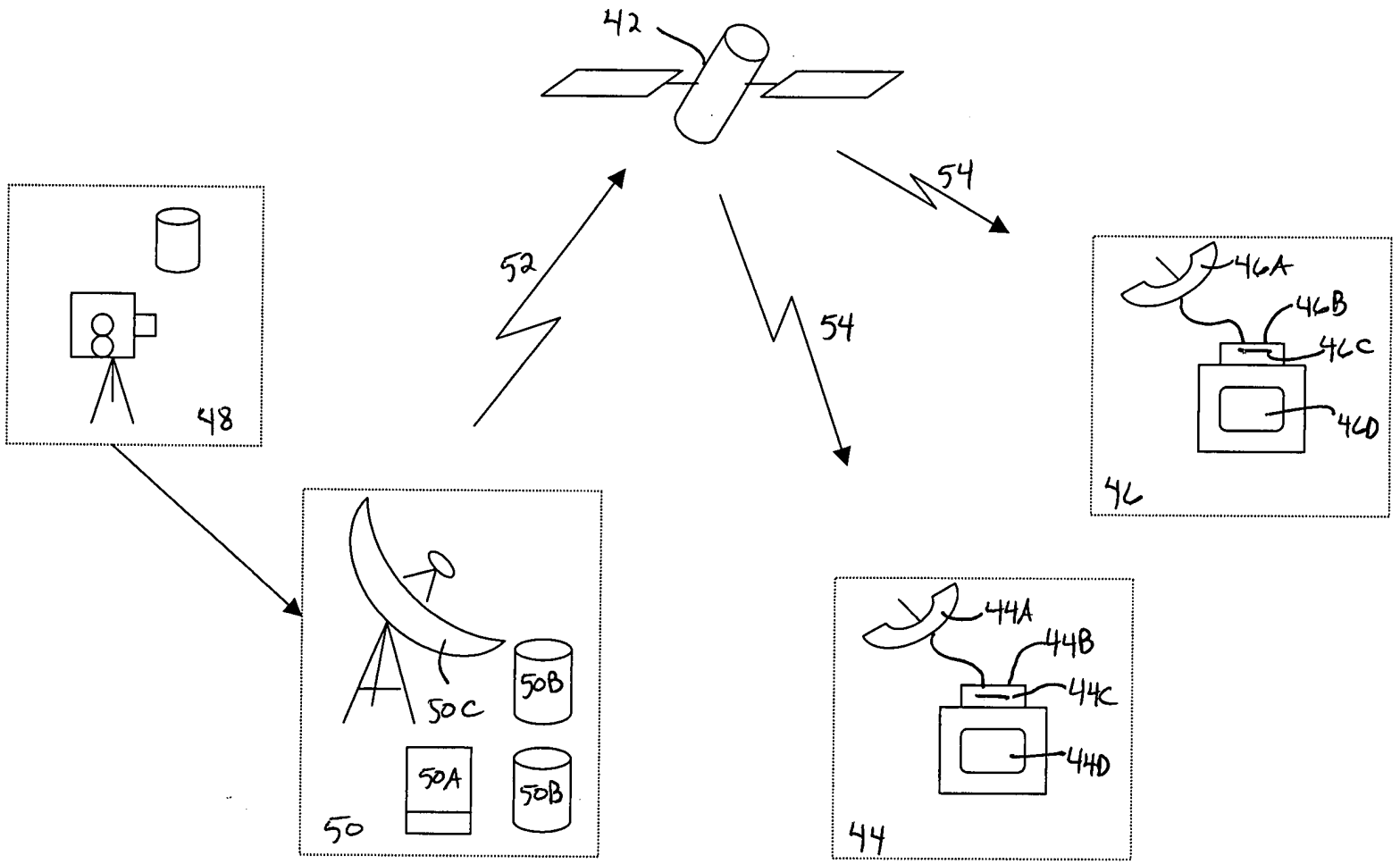
42
52
54
54
48
46A
46B
46C
46D
46
50
50A
50B
50C
50B
44A
44B
44C
44D
44

Figure 2

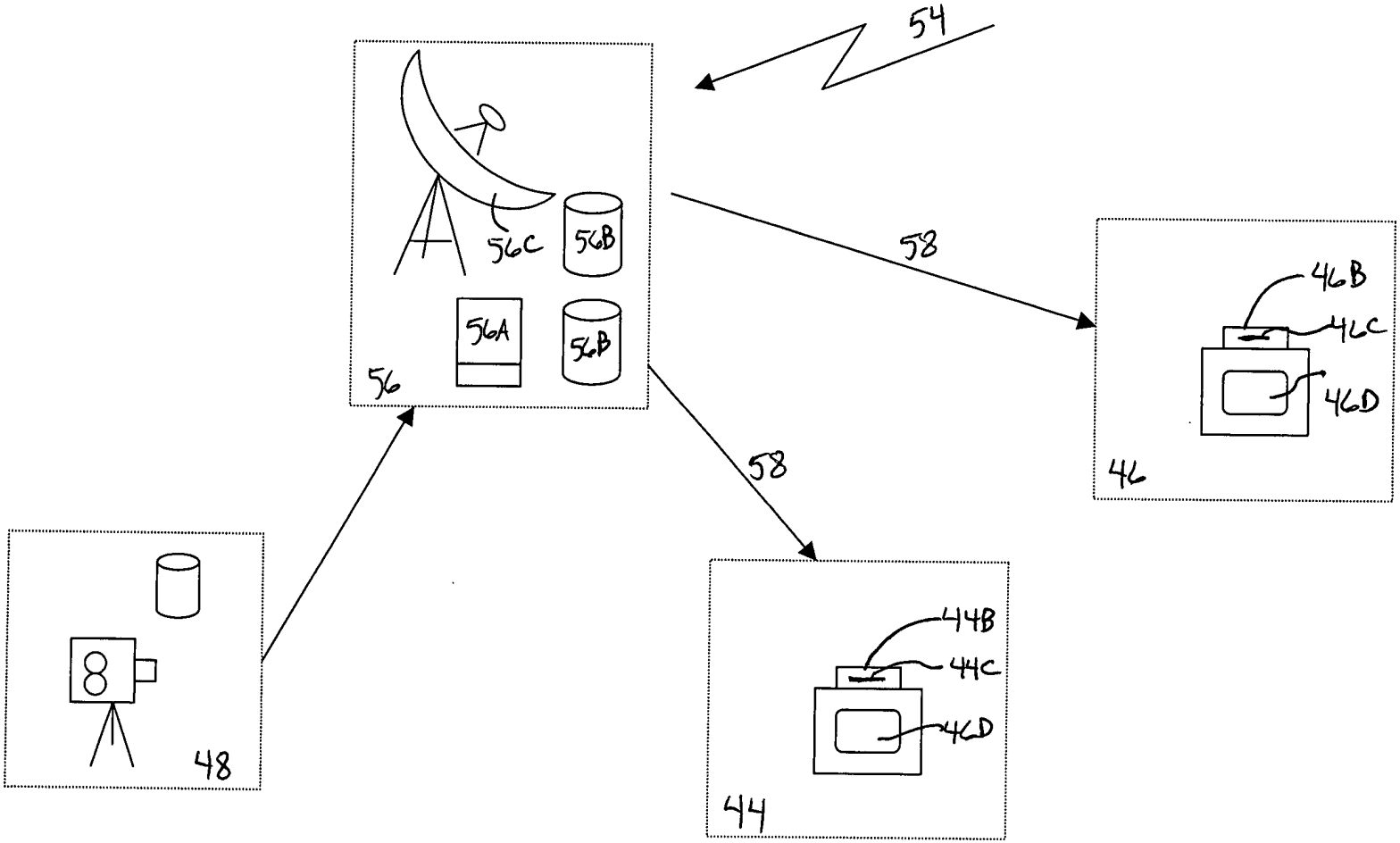

Figure 3

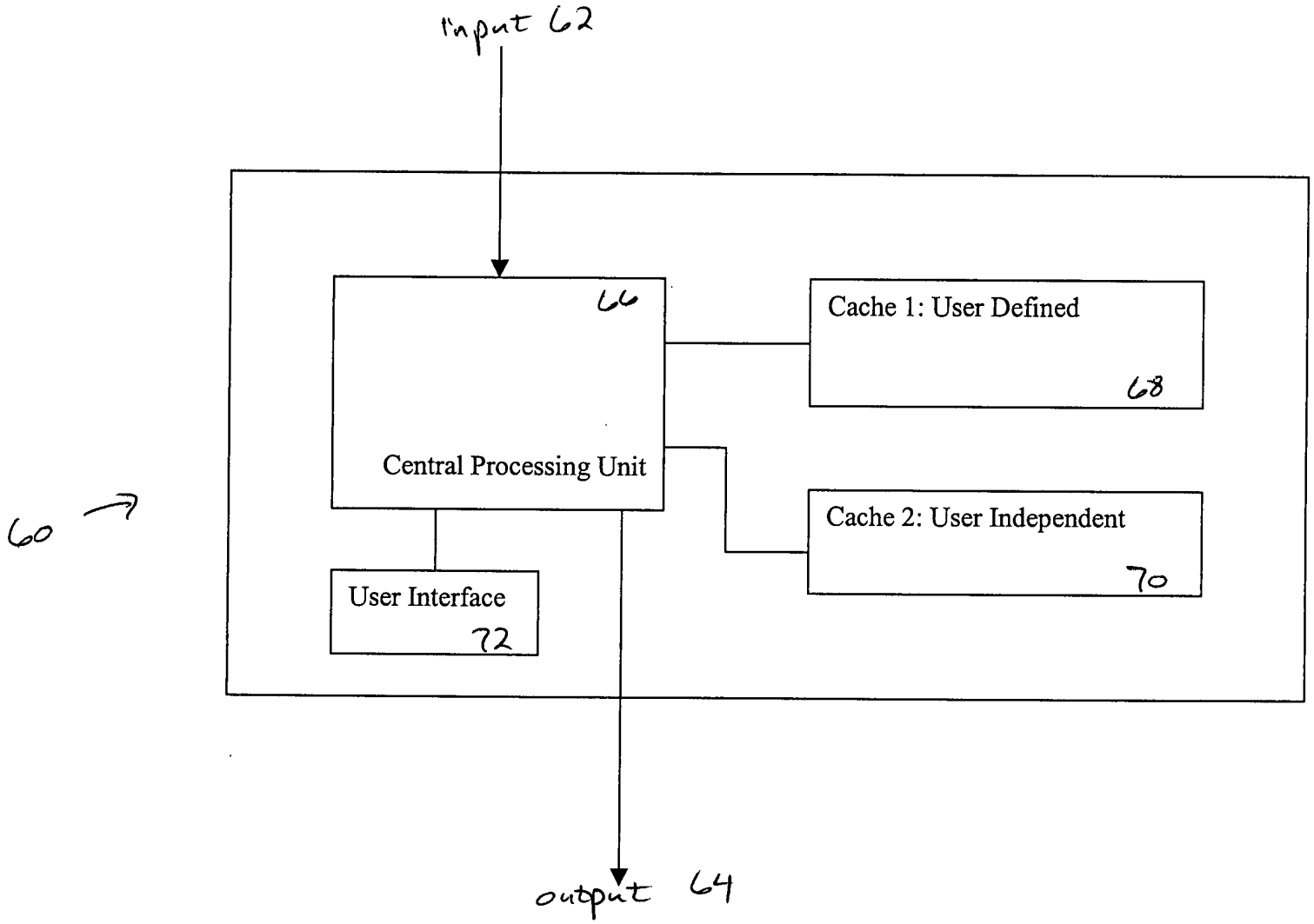
Input 62
66
Central Processing Unit
Cache 1: User Defined
68
Cache 2: User Independent
70
User Interface
72
60
output 64

Figure 4